University of Illinois at Urbana-Champaign
Dept. of Electrical and Computer Engineering

## ECE 120: Introduction to Computing

## Typing in a Number

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## Let's Solve a Problem

Let's see how we can translate

- a task in human terms
- into a program using LC-3 instructions.

Starting with the task, we shall

- identify information that we need to store,
- assign registers for stored values
- draw a flow chart for the code
(roughly at the level of instructions), and
- write instructions in human-readable form.
(The actual program is also available to you.)

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## Overview of Our Task

Let the user type a number

- from 0 to 32767
- using the keyboard, and
- pressing <Enter> when done.

Read in the number, convert it to
2's complement, and store it in memory.
Give errors if

1. a non-digit is pressed, or
2. overflow occurs (> 32767).

## Programs are Finite State Machines

Our first question:

## What do we need to store?

In other words, what information do we need to have handy in order to solve the problem?
Does this question remind you of FSMs?

- Keys are inputs (including <Enter>), - error messages are outputs, and
${ }^{\circ}$ number typed is eventually read out.
- Our program is like an FSM!


## What Information Do We Need to Store?

But back to the question:
What do we need to store?
In other words, what information do we need to have handy in order to solve the problem?

1. the key pressed (one at a time)
2. the current value of the user's number (from previous keystrokes)

## Let's Also Store the Value xFFD0

It's also convenient to store xFFD0.
Any idea why?
$\mathrm{xFFD} 0=-\mathrm{x} 0030=$ ' 0 ' (the 0 digit in ASCII)

## So?

When we want to convert a digit typed in ASCII into a 2's complement value,

- we need to subtract x0030,
- but we can only subtract x0010 with a single ADD instruction.



## Assign a Register for Each Value that We Store

Finally, we need a temporary
value for computations.
Let's assign registers.
When we use GETC (TRAP x20)

- to read a character,
- the keystroke comes back in R0, so
- use R0 for the key pressed.

R1 can be the current value of the number.
R2 can hold xFFD0.
And R3 can be our temporary.

## How Do We Update the Current Value?

When the user presses a key,
how do we update the "current value?"
For example, suppose that
$\circ$ the user has typed $3,2,7$, and 6 , (in that order),

- so the "current value" is 3276 .

If the user presses ' 7 ,' we should

- use 3276 and 7
- to calculate 32767.


## How?

## A Formula for Updating the Current Value

$$
\begin{aligned}
& \text { new value }= 10 \times \text { current value }+ \\
& \text { new digit }
\end{aligned}
$$

Like that?
Good, we can use the LC-3
MULTIPLY instruction.
Oh.
Well, we'll figure it out.
Let's draw a flow chart.

## A Flow Chart for Typing in a Number



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We're Ready to Write Instructions!


## Which Registers Need to Be Initialized?

Here's our table of registers.
R 0 will be filled in
by GETC.
R3 is just a temporary.

| R0 | key pressed |
| :--- | :--- |
| R1 | current value |
| R2 | xFFD0 |
| R3 temporary |  |

We need to set R2 to xFFD0.
What about R1?
(What value should it have before the user presses a key?)

## Derive the Initial Value from the Update Formula

Here is our formula for updating:
new value $=10 \times$ current value + new digit
If the user

- first presses 5,
- we want new value to be 5 , so

$$
5=10 \times \text { current value }+5
$$

What should "current value" be? 0
Ok, so we have to initialize R1 to 0 .

## Code to Initialize Variables

x3000 LD R2, $\qquad$
For R2, we can load the desired value from memory.

> Where in memory? After the program. Where's that? We don't know yet.

## We need to initialize

 R2 to xFFD0 and R1 to 0 .
## Code to Initialize Variables

```
x3000 LD R2,
x3001 AND R1,R1,#0
```

For R1, we can use an AND instruction to set it to 0 .

What's Next? Reading a Character from the Keyboard


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## Code to Read a Character from the Keyboard

```
x3000 LD R2,
```

$\qquad$

``` x3001 AND R1,R1,\#0 x3002 TRAP x20 x3003 TRAP x21
```

TRAP $\times 20$ (GETC) reads a character into RO .

Why TRAP $x 21$, too?

We need to use a TRAP to read a character from the keyboard.

We want the character typed to be echoed to the display (with an OUT trap)!

What's Next? Checking for the <Enter> Key


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Code to Check for the <Enter> Key

| x3000 LD R2, <br> x3001 AND R1,R1,\#0 <br> x3002 TRAP x20 <br> x3003 TRAP x21 <br> x3004 ADD R3,R0,\#-10 | The <Enter> key <br> produces ASClI <br> character \#10. |
| :--- | :---: |
| Subtract \#10 to make <br> R0 is the key <br> aressed. | Where does the <br> result go? Discard it <br> (into R3). |


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Code to Check for the <Enter> Key

| x3000 LD R2, $\qquad$ x3001 AND R1,R1,\#0 |  |
| :---: | :---: |
|  |  |
| 002 TRAP x20 | character \#10. |
| x3003 TRAP x21 |  |
| x3004 ADD R3,R0,\#-10 <br> x 3005 BRz $\qquad$ |  |
| If the result is 0 , the key pressed (R0) was <Enter>. | the number, which is ... somewhere. We'll leave the offset blank. |

The <Enter> key produces ASCII character \#10.
x3003 TRAP x21
x3004 ADD R3,R0,\#-10 x3005 BRz $\qquad$ Let's branch on zero to the code that stores he number, which is leave the offset blank.

## What's Next? Checking for a Digit



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## Code to Check for a Digit

x3000 LD R2, $\qquad$ Remember that R2
has negative ASCII
digit 0 (xFFD0). x3002 TRAP x20
x3003 TRAP x21 x3004 ADD R3,R0,\#-10 x 3005 BRz $\qquad$ x3006 ADD R0,R0,R2 x3007 BRn $\qquad$
Where to go? Later.

If the result is below 0 (negative), the original character was less than x30, and thus not a digit.

## Code to Check for a Digit

| x3000 LD R2, | Now R0 holds the key minus x30. If a digit, 0 to 9 . |
| :---: | :---: |
| x3001 AND R1,R1,\#0 |  |
| x 3002 TRAP x20 |  |
| x3003 TRAP x21 |  |
| x3004 ADD R3,R0,\#-10 | What about keys greater than ' 9 '? |
| x 3005 BRz |  |
| x3006 ADD R0,R0,R2 |  |
| x 3007 BRn | Let's subtract \#10 (and discard the result). |
| x3008 ADD R3,R0,\#-10 |  |


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What's Next? Multiplying by 10


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## Code to Multiply R1 by 10

x3000 LD R2, $\qquad$ x3001 AND R1,R1,\#0 x3002 TRAP x20 x3003 TRAP x21 x3004 ADD R3,R0,\#-10 x 3005 BRz $\qquad$ x3006 ADD R0,R0,R2 x3007 BRn $\qquad$ x3008 ADD R3,R0,\#-10 x3009 BRzp $\qquad$ -

## Multiply the current value (R1) by 10.

Anyone want to learn ARM now? Or maybe x86? It has "MULT!"

Let me just write some code...

Code to Multiply R1 by 10

> x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1

> Multiply the current value (R1) by 10 .

## Look good?

## Great.

Let's move on?

## Code to Multiply R1 by 10

; This is a comment.
x300A ADD R3,R1,R1
; Now R3 has 2V.
x300B ADD R3,R3,R3
; Now R3 has 4V.
x300C ADD R1,R1,R3
; Now R1 has 5V.
x300D ADD R1,R1,R1
; Now R1 has 10 V .

## Multiply the current value (R1) by 10.

Let's use V to denote the original value of R1.


What's Next? Adding the New Digit


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## Code to Add the New Digit

x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0

Add the new digit into R1.

Where is the new digit? We already computed it and stored it in R0.

What's Next? Checking for Overflow


## Download the Code or Printout to See the Overflow Checks

How do we check for overflow?
Unfortunately, it's not so easy.
Checking for overflow requires checking all of the ADD instructions.

We won't do that here.
To see the overflow checks, look at
the full version provided to you.

## Code to Add the New Digit

x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0 x300F BRnzp $\qquad$
How? Use an unconditional branch.

Let's figure out the offset later.

What's Next? Storing the Number


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## Code to Store the Number

x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0
x300F BRnzp $\qquad$ x3010 ST R1, $\qquad$

## Store the number to memory.

Let's use an ST to store it nearby.

The number is in R1.

Where? Well... later.
$\qquad$
x300A ADD R3,R1,R1
x300A ADD R3,R1,R1
x300B ADD R3,R3,R3
x300B ADD R3,R3,R3
x300C ADD R1,R1,R3
x300C ADD R1,R1,R3
x300D ADD R1,R1,R1
x300D ADD R1,R1,R1
x300E ADD R1,R1,R0
x300E ADD R1,R1,R0
x300F BRnzp
x300F BRnzp
Use a HALT trap
Use a HALT trap
(number x25).
(number x25).

## Code for Data

x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0 x300F BRnzp $\qquad$ x3010 ST R1, $\qquad$ x3011 TRAP x25 x3012 xFFD0 x3013 place for number

But we still need a couple more things.

First, we need the value xFFD0

Second, we need a place to store the number.

## Here's the Whole Program

```
x3000 LD R2,
```

$\qquad$

```
x300A ADD R3,R1,R1
x300B ADD R3,R3,R3
x300C ADD R1,R1,R3
x300D ADD R1,R1,R1
x300E ADD R1,R1,R0
x300F BRnzp
x3010 ST R1,
x3011 TRAP x25
x3012 xFFD0
x3013 place for number
```

Now for Some Real Fun!

It's time for...
Well, yes, we'll turn them into bits.
But I meant counting!
Almost as exciting as bits.

## Help Me Count (Please!)

```
x3000 LD R2, x11
x3001 AND R1,R1,#0
x3002 TRAP x20
x3003 TRAP x21
x3004 ADD R3,R0,#-10
x3005 BRz
```

$\qquad$

``` x3006 ADD R0,R0,R2 x3007 BRn
``` \(\qquad\)
``` x3008 ADD R3,R0,\#-10 x3009 BRzp
``` \(\qquad\)
``` \(-10>x 3\) x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0 x300F BRnzp
``` \(\qquad\)
``` x3010 ST R1,
``` \(\qquad\)
``` x3011 TRAP x25 x3012 xFFD0 x3013 place for number
```

—PC will point here

## Help Me Count (Please!)

| x3000 LD R2, x11 | x300A ADD R3,R1,R1 |
| :--- | :--- |
| x3001 AND R1,R1,\#0 | x300B ADD R3,R3,R3 |
| x3002 TRAP x20 | x300C ADD R1,R1,R3 |
| x3003 TRAP x21 | x300D ADD R1,R1,R1 |
| x3004 ADD R3,R0,\#-10 | x300E ADD R1,R1,R0 |
| x3005 BRz xA | x300F BRnzp |
| x3006 ADD R0,R0,R2 | x3010 ST R1, |
| x3007 BRn $\times$ xC  <br> x3008 ADD R3,R0,\#-10 x3011 TRAP x25 <br> x3012 xFFD0  <br> x3009 BRzp x3013 place for number <br> PC will point here  |  |

PC will point here

## Help Me Count (Please!)

```
x3000 LD R2, x11
x3001 AND R1,R1,#0
x3002 TRAP x20
x3003 TRAP x21
x3004 ADD R3,R0,#-10
```



```
x3006 ADD R0,R0,R2 & x3010 ST R1,
x3007 BRn_ x3011 TRAP x25
x3008 ADD R3,R0,#-10 x3012 xFFD0
x3009 BRzp
```

$\qquad$

```
PC will point here
```

x3013 place for number
x300A ADD R3,R1,R1 x300B ADD R3,R3,R3 x300C ADD R1,R1,R3 x300D ADD R1,R1,R1 x300E ADD R1,R1,R0 x300F BRnzp $\qquad$ x300 BRnzp $\qquad$ x3010 ST R1, x3011 TRAP x25 x3012 xFFD0

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## Help Me Count (Please!)

> x3000 LD R2, x11 x3001 AND R1,R1,\#0 x3002 TRAP x20 x3003 TRAP x21 x3004 ADD R3,R0,\#-10 x3005 BRz xA x3006 ADD R0,R0,R2 x3007 BRn xC x3008 ADD R3,R0,\#-10 x3009 BRzp xA PC will point here

## Help Me Count (Please!)

```
x3000 LD R2, x11 x300A ADD R3,R1,R1
x3001 AND R1,R1,#0 x300B ADD R3,R3,R3
x3002 TRAP x20 x300GADD R1,R1,R3
x3003 TRAP x21 x300D AND R1,R1,R1
x3004 ADD R3,R0,#-10 x300E ADD R1,R1,R0
x3005 BRz xA x300F BRnzp x-E
x3006 ADD R0,R0,R2
x3007 BRn xC
x3008 ADD R3,R0,#-10
x3009 BRzp xA
PC will point here
x3010 ST R1,
x3011 TRAP x25
x3012 xFFD0
x3013 place for number
```

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## Now We Can Write Bits!

| x3000 LD R2, x11 | x300A ADD R3,R1,R1 |
| :--- | :--- |
| x3001 AND R1,R1,\#0 | x300B ADD R3,R3,R3 |
| x3002 TRAP x20 | x300C ADD R1,R1,R3 |
| x3003 TRAP x21 | x300D ADD R1,R1,R1 |
| x3004 ADD R3,R0,\#-10 | x300E ADD R1,R1,R0 |
| x3005 BRz xA | x300F BRnzp x-E |
| x3006 ADD R0,R0,R2 | x3010 ST R1, x2 |
| x3007 BRn xC | x3011 TRAP x25 |
| x3008 ADD R3,R0,\#-10 | x3012 xFFD0 |
| x3009 BRzp xA | x3013 place for number |

## Help Me Count (Please!)

| x3000 LD R2, x11 | x300A ADD R3,R1,R1 |
| :--- | :--- |
| x3001 AND R1,R1,\#0 | x300B ADD R3,R3,R3 |
| x3002 TRAP x20 | x300C ADD R1,R1,R3 |
| x3003 TRAP x21 | x300D ADD R1,R1,R1 |
| x3004 ADD R3,R0,\#-10 | x300E ADD R1,R1,R0 |
| x3005 BRz xA | x300F BRnzp x-E |
| x3006 ADD R0,R0,R2 | x3010 ST R1, x2 |
| x3007 BRn xC | x3011 TRAP x25 |
| x3008 ADD R3,R0,\#-18 | x3012 xFFD0 |
| x3009 BRzp xA | x3013 place for number |
| PC will point here |  |

## Encode the Instruction at x3000 into Bits

```
x3000 LD R2, x11
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{4}{|r|}{opcode} & \multicolumn{3}{|l|}{destination register} & \multicolumn{9}{|l|}{9-bit 2's complement offset} \\
\hline 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\
\hline \multicolumn{4}{|c|}{LD} & \multicolumn{3}{|c|}{R2} & \multicolumn{9}{|c|}{x11} \\
\hline
\end{tabular}
```

In a program, we include the spaces between bits (more readable for humans) and add a comment, either "LD R2,x11" or "R2 $2 \leftarrow \mathrm{M}[\mathrm{PC}+\mathrm{x} 0011]$ ]"

## A Binary File Starts with the Starting Address

Also, the starting address, x3000, goes first.
For example...
0011000000000000 ; start at $\times 3000$
0010010000010001 ; LD R2,x11
; and so forth...
destination
opcode register 9-bit 2's complement offset

| 0 | 0 | 1 | 0 | 0 | 1 | 0 |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |

LD
R2,
$\times 11$

Encode the Instruction at x3001 into Bits

| opcode |  |  |  | destination register |  |  | source register 1 |  |  | mode | 5-bit <br> 2's complement |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| AND |  |  |  |  | R1, |  |  | R1, |  |  |  |  | \#0 |  |  |

The Rest is Left to You

I'll leave the rest for you.
I think you can manage it.
Look at the LC-3 encoding table, and write the bits.
Compare your answers with the code provided on the web page.

